

Appl. No. 10/709,524  
Amdt. dated December 22, 2004  
Reply to Office action of December 02, 2004

**Amendments to the Specification:**

Please replace paragraph [0008] with the following amended paragraph:

- 5 [0008] Briefly described, the claimed invention discloses a semiconductor device with a negative voltage regulator. The semiconductor device includes a negative voltage regulator capable of regulating a negative input voltage and outputting a negative output voltage at ~~an~~ a first output node. The negative voltage regulator comprises a driver for adjusting the negative output voltage, a first operational amplifier capable of outputting a driving voltage for controlling a current of a first transistor included in the driver according to a feedback voltage and a first reference voltage, a second operational amplifier capable of outputting a driving voltage for controlling a current of a second transistor included in the driver according to a second reference voltage and the feedback voltage, a current source circuit comprising two triple-well NMOS transistors and capable of providing the driver a current, and a voltage potential divider capable of generating the feedback voltage by dividing potentials of a second voltage source and the negative output voltage and outputting the feedback voltage to the first operational amplifier and the second operational amplifier for adjusting the current on the first transistor and the current on the second transistor and thereby regulating the negative output voltage.
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